

FIG. 1

பொது மனத விதம் கீழ்க்கண்ட பார்த்தலை மற்றும் அதன் மூலம் நிர்ணயித்தலை கொண்டு வருகின்றது.

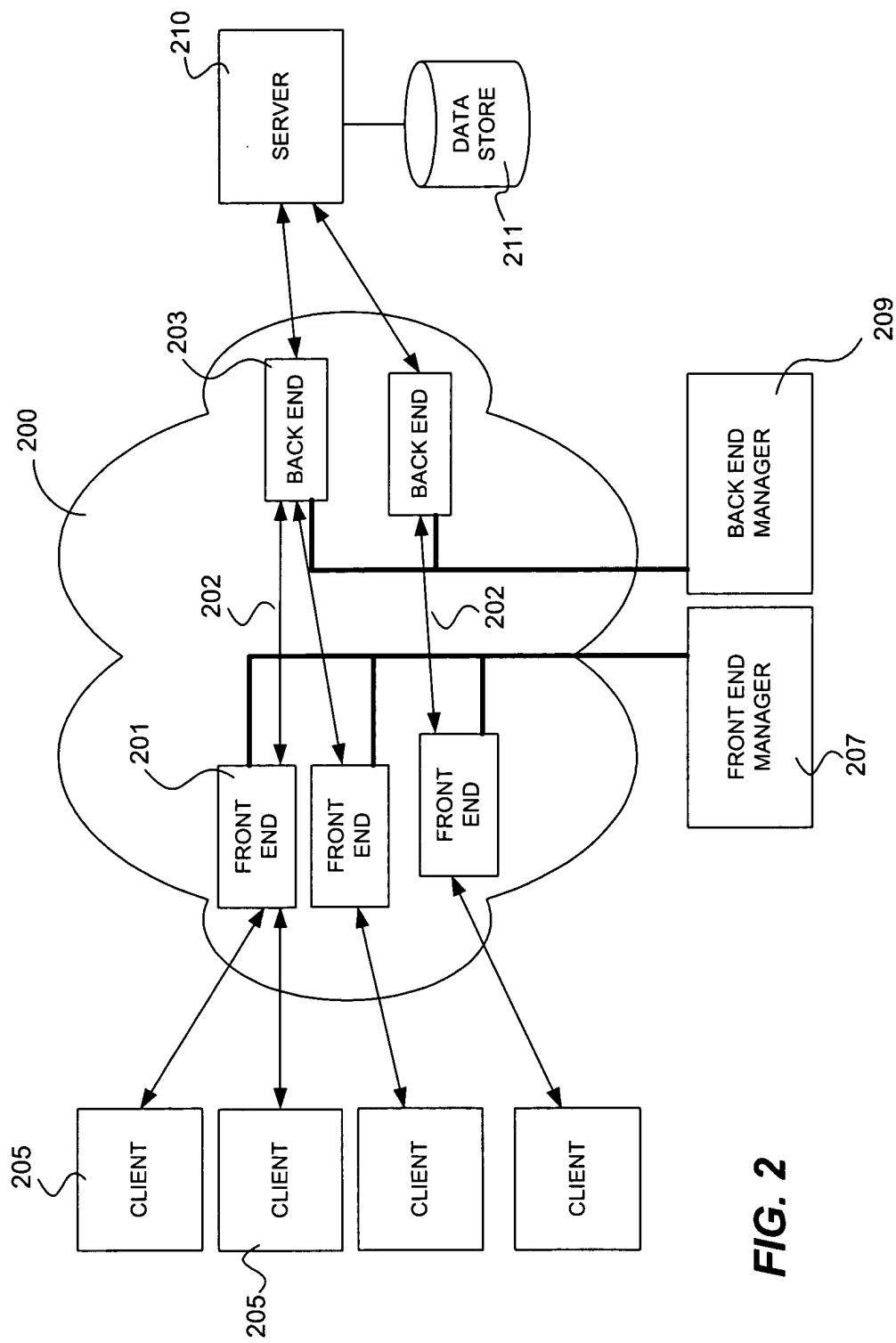


FIG. 2

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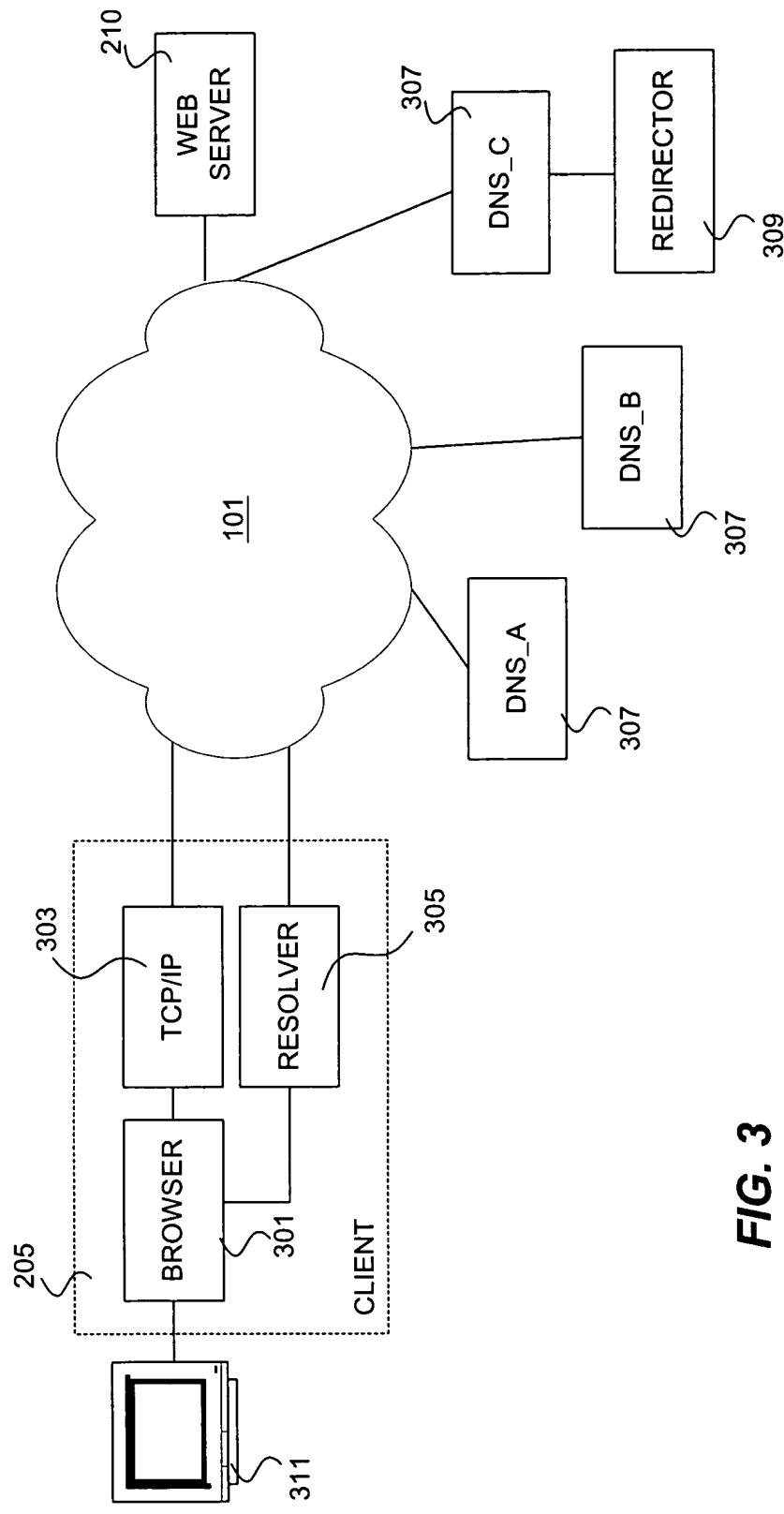


FIG. 3

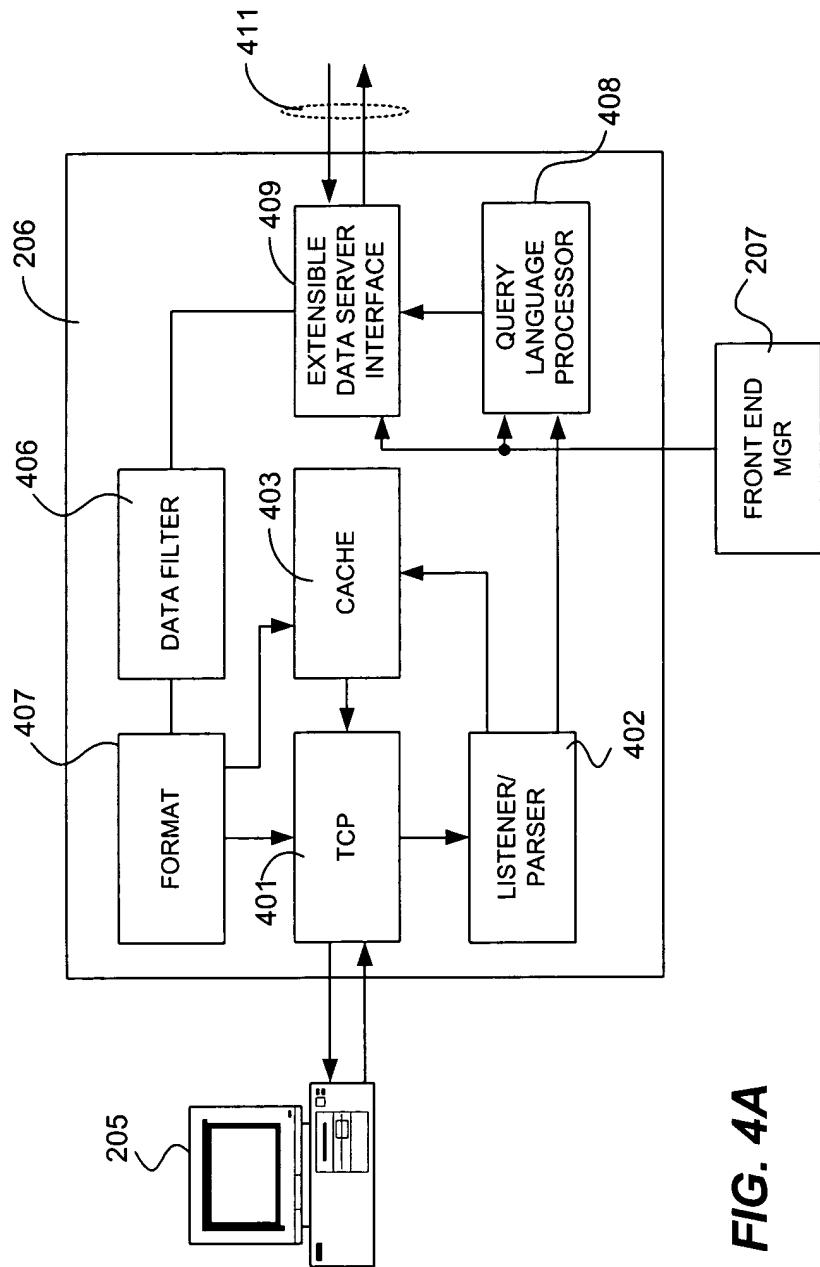


FIG. 4A

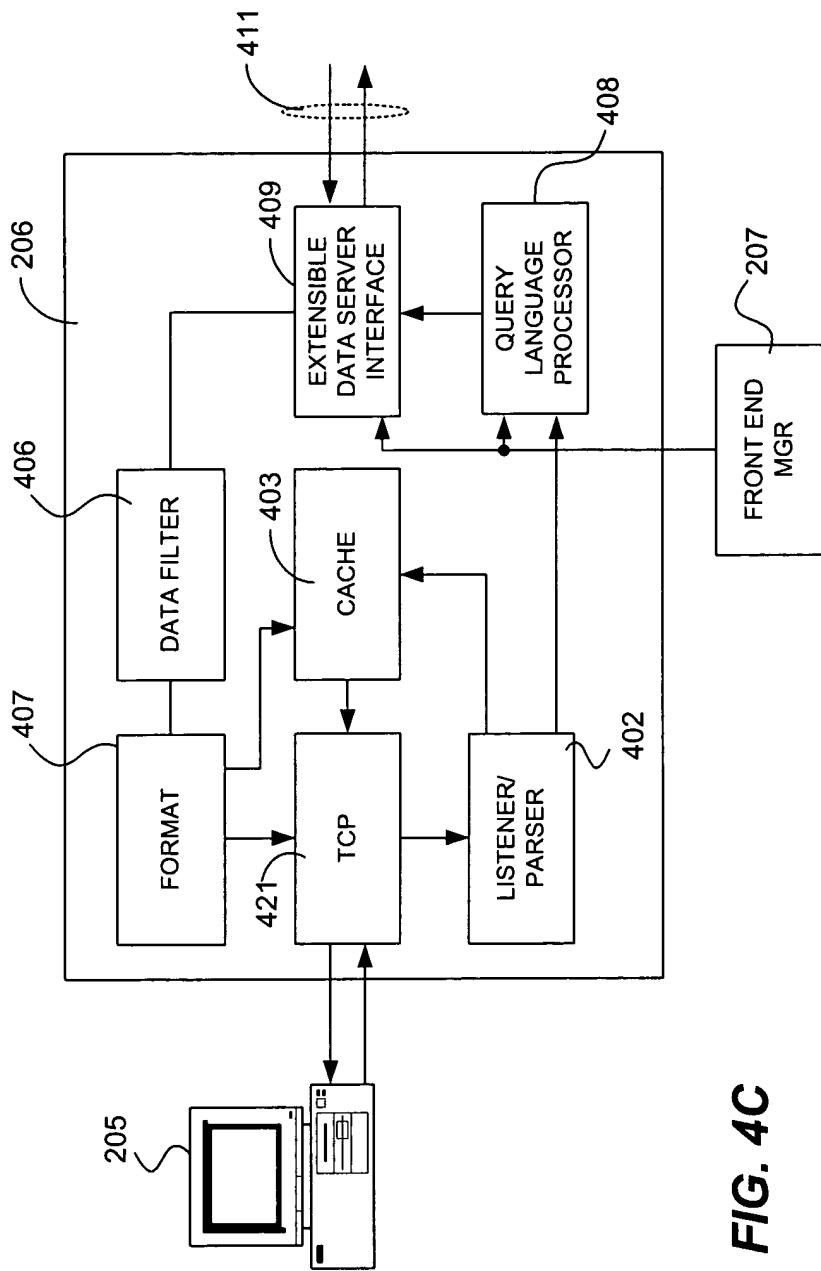


FIG. 4C

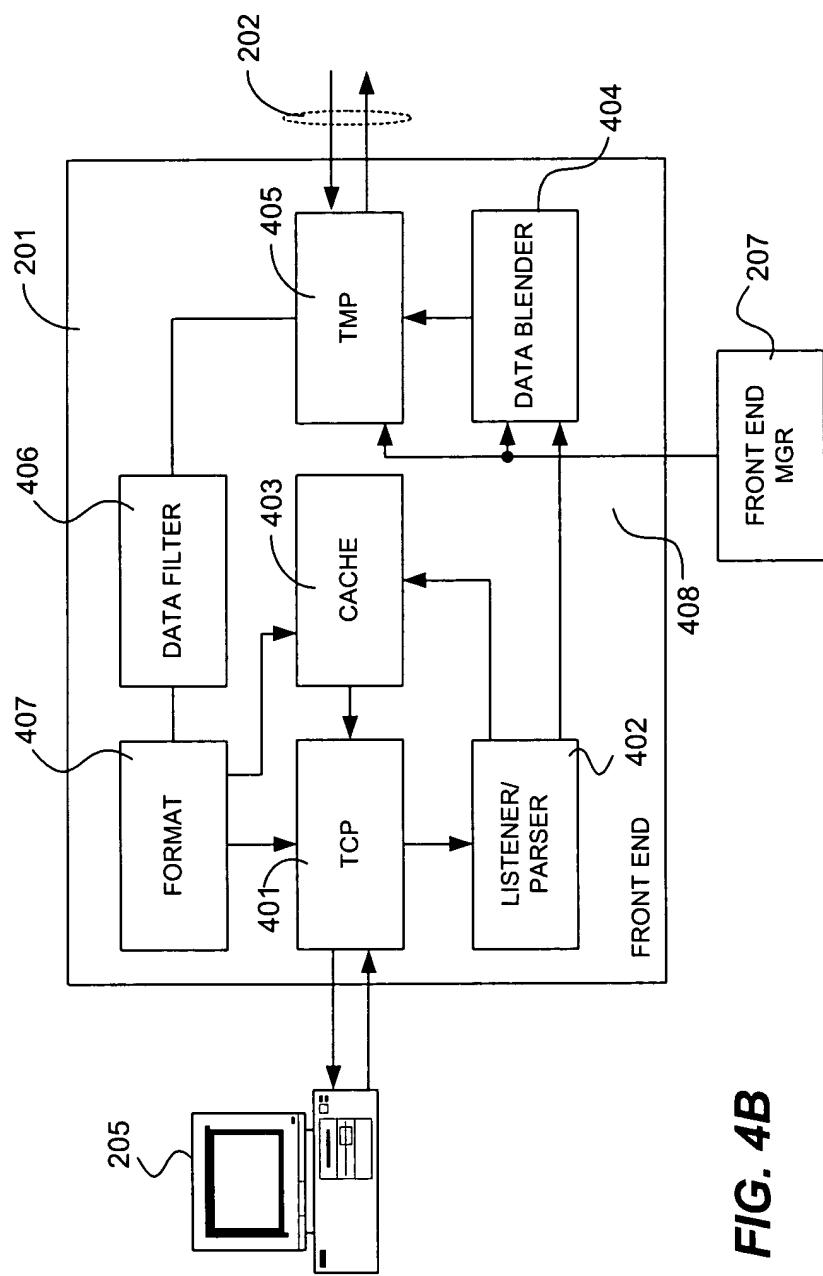


FIG. 4B

FIG. 5
A block diagram of a system architecture. At the top, a terminal unit 210 is connected to an Extensible Data Server Interface 501. The interface 501 is connected to a BACK END 507. Inside the BACK END 507, there is a CACHE 503, a DATA FILTER 502, a DATA BLENDER 504, and a TMP 505. The BACK END 507 is connected to a BACK END MGR 209 via a connection 203. The BACK END MGR 209 is connected to the DATA BLENDER 504 via a connection 504. The DATA BLENDER 504 is connected to the DATA FILTER 502 via a connection 502. The DATA FILTER 502 is connected to the EXTENSIBLE DATA SERVER INTERFACE 501 via a connection 501. The EXTENSIBLE DATA SERVER INTERFACE 501 is connected to the BACK END MGR 209 via a connection 210. The BACK END MGR 209 is also connected to the BACK END 507 via a connection 202. The BACK END 507 is connected to the QUERY LANGUAGE PROCESSOR 506 via a connection 506. The QUERY LANGUAGE PROCESSOR 506 is connected to the BACK END MGR 209 via a connection 507.

FIG. 5

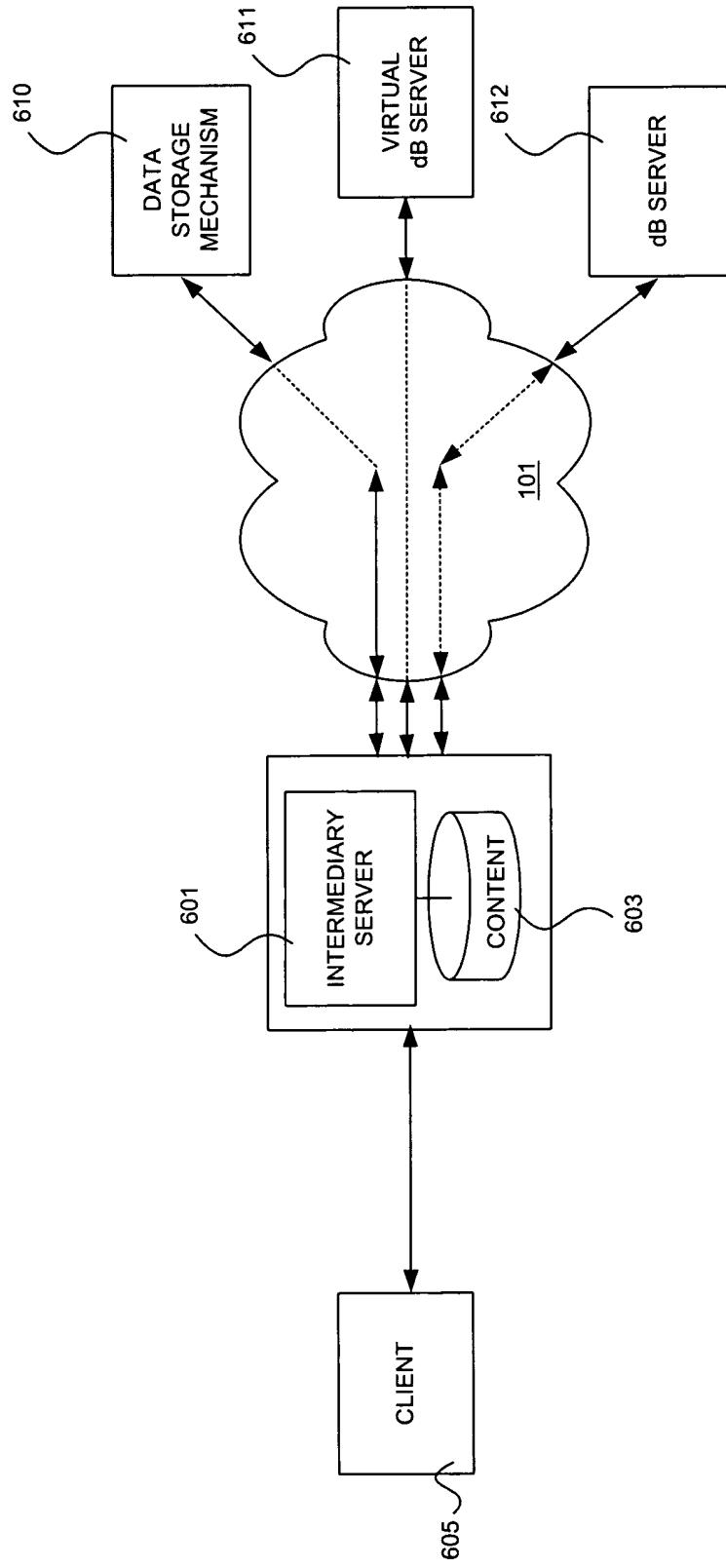


FIG. 6